

## IN THE CLAIMS

Please cancel claims 20-47.

For the Examiner's convenience, all pending claims are included below.

1. (Original) A method comprising:  
  
reading a time of exiting a reduced power consumption state prior to an execution of an interrupt routine;  
  
storing the time of exiting the reduced power consumption state in a register; and  
  
calculating a reduced power consumption state duration based on the time of exiting the reduced power consumption state stored in the register.
2. (Original) The method of claim 1 wherein the reduced power consumption state is a C1 power state.
3. (Original) The method of claim 1 further comprising:  
  
reading a time of entering the reduced power consumption state;  
  
storing the time of entering the reduced power consumption state in a main memory; and  
  
calculating the reduced power consumption state duration utilizing the time of entering and the time of exiting the reduced power consumption state.
4. (Original) The method of claim 1 wherein the register is located in a chipset.
5. (Original) The method of claim 1 wherein the register is located in a processor.

6. (Original) A method comprising:  
starting a time counter;  
entering a reduced power consumption state;  
halting the time counter prior to an execution of an interrupt routine; and  
exiting the reduced power consumption state.
7. (Original) The method of claim 6 wherein the starting the time counter comprises requesting a chip to start a time counter.
8. (Original) The method of claim 6 wherein the halting the time counter comprises requesting a chip to halt the time counter.
9. (Original) The method of claim 7 wherein the chip is a personal computer chipset.
10. (Original) The method of claim 8 wherein the chip is a personal computer chipset.
11. (Original) The method of claim 6 wherein the exiting the reduced power consumption state comprises executing the interrupt routine.
12. (Original) The method of claim 6 wherein the time counter comprises a reduced power consumption state duration.
13. (Original) The method of claim 6 wherein the reduced power consumption state is a C1 power state.

14. (Original) A method comprising:
- storing a time of entering a reduced power consumption state in a chip;
- storing a time of exiting the reduced power consumption state in the chip prior to an execution of an interrupt routine; and
- automatically calculating a reduced power consumption state duration.
15. (Original) The method of claim 14 wherein the storing the time of entering the reduced power consumption state comprises storing the time of entering in a register.
16. (Original) The method of claim 14 wherein the storing the time of exiting the reduced power consumption state comprises storing the time of exiting in a register.
17. (Original) The method of claim 14 wherein the automatically calculating the reduced power consumption state duration is performed by the chip.
18. (Original) The method of claim 17 wherein the chip is a personal computer chipset.
19. (Original) The method of claim 14 wherein the reduced power consumption state is a C1 power state.
- 20-47 (Cancelled)